



Form NO-1449 U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE	ATTY DOCKET NO. 0325.00459	SERIAL NO. 09/823,414
INFORMATION DISCLOSURE STATEMENT BY APPLICANT	APPLICANT: Scott Cordner	
	FILING DATE March 31, 2001	GROUP 2181

## U.S. PATENT DOCUMENTS

Examiner Initial	Document No.	Date	Name	Class	Sub-Class	Filing Date
VM	4 6 8 9 7 4 0	8/25/1987	Moelands et al.	364	200	
	5 5 5 9 5 0 2	9/24/1996	Schutte	340	825.21	
	5 6 8 9 1 9 6	11/18/1997	Schutte	326	86	
	5 9 7 4 4 8 6	10/26/1999	Siddappa	710	53	
	6 0 1 2 1 0 3	1/4/2000	Sartore et al.	710	8	
	6 1 4 5 0 4 5	11/7/2000	Falik et al.	710	129	
	6 1 4 8 3 5 4	11/14/2000	Ban et al.	710	102	
	6 1 7 3 3 5 5	1/9/2001	Falik et al.	710	129	

## FOREIGN PATENT DOCUMENTS

	Document No.	Date	Country	Class	Sub-Class	Transl.	
						Yes	No
WO 96	/ 1 7 3 0 5	06.06.96	PCT	G06F		X	
WO 99	/ 0 9 7 1 2	25.02.99	PCT	H04L		X	
WO 98	/ 3 4 3 7 6	06.08.98	PCT	H04L		X	

## OTHER DOCUMENTS (Incl. Author, Title, Date, Pertinent Pages, etc.)

VM	Larky, Steven P., et al., "DEDICATED CIRCUIT AND METHOD FOR ENUMERATING AND OPERATING A PERIPHERAL DEVICE ON A UNIVERSAL SERIAL BUS (USB)", U.S. Serial No. 09/232,578, Filed 1/16/99.
VM	"CYPRESS INTELLIGENT SERIAL INTERFACE ENGINE PROVIDES USB 2.0 CONNECTIVITY TO EMBEDDED MICROCONTROLLERS", Cypress Press Release, 3/17/01, pp. 1-2.
VM	"ISP1581 UNIVERSAL SERIAL BUS 2.0 HIGH-SPEED INTERFACE DEVICE", October 23, 2000, Rev. 02, pp. 1-73.
VM	"CY7C68001 EZ-USB SX2 HIGH-SPEED USB INTERFACE DEVICE", January 4, 2001, pp. 1-29.
VM	"UNIVERSAL SERIAL BUS SPECIFICATION", Revision 1.1, September 23, 1998, pp. 1-311.
VM	"UNIVERSAL SERIAL BUS SPECIFICATION", Revision 2.0, April 27, 2000, pp. 1-622.

Examiner <i>W. W. Dwyer</i>	Date Considered <i>10/20/03</i>
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Examiner: Initial if references considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.